

## OV7930 Color CMOS Analog CAMERACHIP™

### General Description

The OV7930 (color) is a high performance quarter-inch CMOS CAMERACHIP™ designed for all applications requiring a small footprint, low voltage, low power consumption and low cost color video camera.

The device supports NTSC composite video output and can directly interface with a VCR TV monitor or other 75 ohm terminated input with 2X standard TV signal range. The OV7930 CAMERACHIPS require only a single 5-volt DC power supply and have been designed for very low power operation.



**Note:** The OV7930 is available in a lead-free package.

### Features

- Single chip 1/4" lens video camera
- Composite video: NTSC
- Sensitivity boost (+27 dB)/AGC ON/OFF
- Automatic exposure/gain/white balance
- External frame sync capability
- Aperture correction
- SCCB programmable controls:
  - Color saturation
  - Brightness
  - Hue
  - White balance
  - Exposure
  - Gain
  - Gamma curve
  - Aperture correction
- Gamma correction (0.45) ON/OFF
- Low power consumption
- +5 volt only power supply
- Wide dynamic range, anti-blooming, zero smearing

### Ordering Information

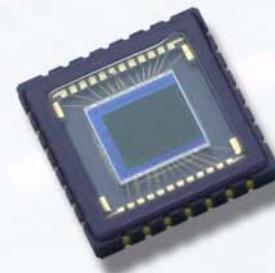
Product	Packages
OV07930-C11A (Color, NTSC, CVO)	CLCC-28
OV07930-P11A (Color, NTSC, CVO)	PLCC-28
OV07930-W	Wafer

### Applications

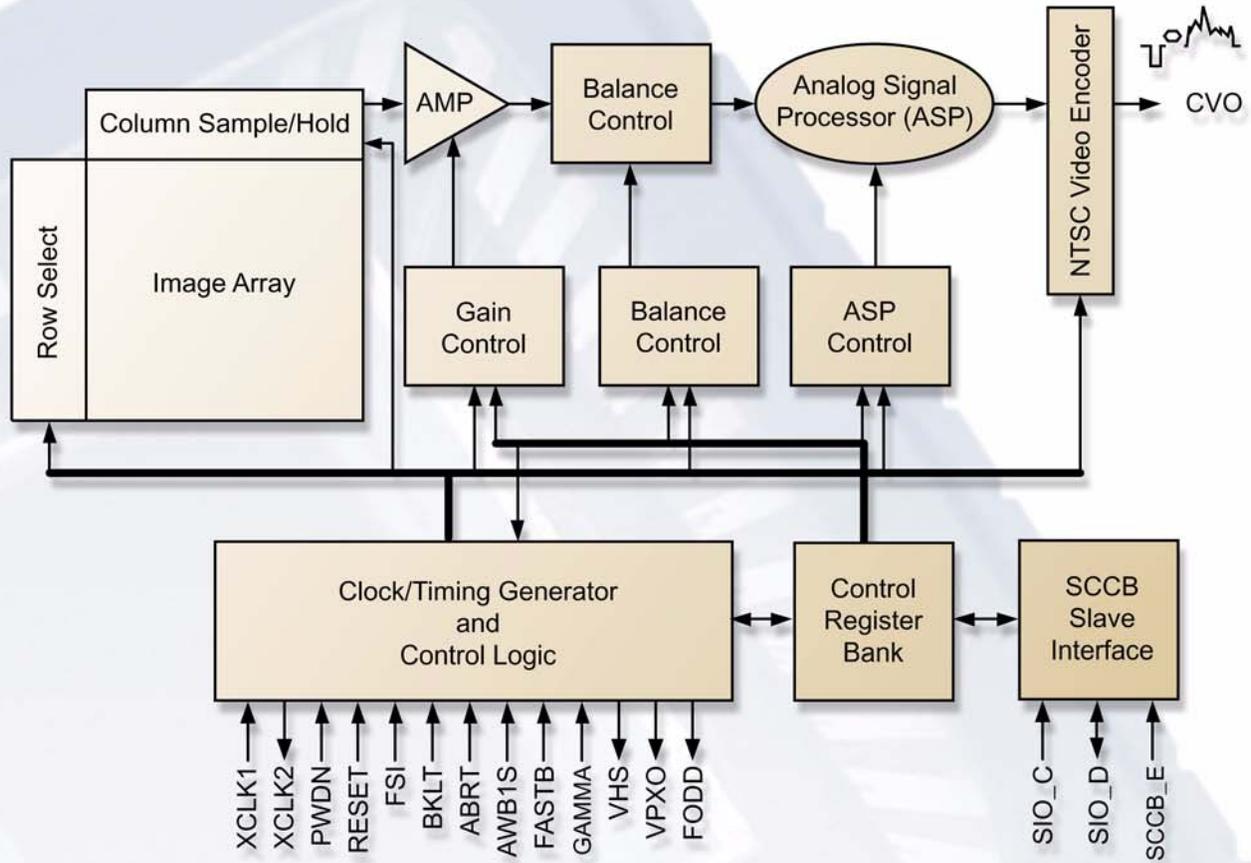
- Video Conferencing
- Video Phones
- Video E-mail
- PC Multimedia
- Toys
- Security
- Surveillance
- Finger Printing
- Medical Equipment
- Dental Equipment

### Key Specifications

	<b>Array Size</b>	510 x 492
	<b>Power Supply</b>	5 VDC ± 5%
<b>Power Requirements</b>	<b>Without Loading</b>	20 mA
	<b>With 75 ohm Loading</b>	< 35 mA
	<b>Image Area</b>	4.00 mm x 3.08 mm
	<b>Auto Electronic Exposure Time</b>	1/60s - 6.3µs
	<b>Minimum Illumination (3000K)</b>	< 2.0 Lux
	<b>Optical Format</b>	1/4"
	<b>S/N Ratio</b>	> 46 dB
	<b>Dynamic Range</b>	> 70 dB
	<b>Pixel Size</b>	7.86 µm x 6.25 µm
	<b>Dark Current</b>	< 100 mV/s
	<b>Fixed Pattern Noise</b>	< 0.03% V <sub>PEAK-TO-PEAK</sub>
	<b>Package Dimensions</b>	0.45 in. x 0.45 in.



## Functional Block Diagram



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